



## Skyworks Unveils Industry's First Clocks for Ethernet and PCI Express® With 18fs RMS Phase Jitter

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### **SKY63104/5/6 Family of Jitter Attenuating Clocks and SKY62101 Ultra-low Jitter Clock Generators Available Now**

IRVINE, Calif.--(BUSINESS WIRE)--Jun. 25, 2025-- Skyworks Solutions, Inc. (Nasdaq: SWKS), an innovator of high-performance analog and mixed-signal semiconductors connecting people, places and things, today announced its latest timing products, the [SKY63104/5/6 family of jitter attenuating clocks](#) and the [SKY62101 ultra-low jitter clock generator](#). Based on Skyworks' fifth generation DSPLL® and MultiSynth™ technologies, these devices enable any-frequency, any-output clock generation and single-IC clock tree solutions for the most demanding networking, data center and industrial applications.

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The SKY63104/5/6 family of jitter attenuating clocks and SKY62101 clock generators are the industry's first clock devices that can simultaneously generate Ethernet and PCI Express® (PCIe) spread

spectrum clocks with unparalleled ultra-low jitter performance. These products provide ultra-low jitter reference clocks for 224G PAM4 Ethernet SerDes with 18fs RMS phase jitter, while generating standards-compliant spread spectrum PCIe clocks (PCIe Gen 1/2/3/4/5/6). Outputs can be independently configured for Ethernet, PCIe and/or general-purpose clocking, making them suitable for a wide range of applications, including:

- Internet infrastructure: Optical and Ethernet networking equipment, including edge/core routers and switches and OTN equipment
- Data centers: Spine and leaf switches, top-of-rack switches, SmartNIC Cards, and AI accelerators
- Industrial: Medical imaging, test and measurement, audio/video broadcast/distribution systems, and satellite communications

"We are excited to introduce the SKY63104/5/6 family of jitter attenuating clocks and the SKY62101 ultra-low jitter clock generator," said James Wilson, vice president and general manager for Mixed Signal Solutions at Skyworks. "Our advanced DSPLL® and MultiSynth™ technologies set a new benchmark for performance and integration, enabling our customers to greatly simplify clock generation in their most demanding applications."

The explosive growth in network bandwidth, cloud computing, and AI in data centers is driving the adoption of 112G/224 PAM4 SerDes technology in 800G/1200G/1600G networks, compelling the need for ultra-low jitter reference clocks. Additionally, PCIe links used for chip-to-chip communication in data center applications are getting faster and faster. PCIe Gen 6 supports a data rate of 64GT/s, doubling the necessary bandwidth requirements over the previous Gen 5 and creating demand for lower-jitter PCIe reference clocks.

The SKY63104/5/6 family of jitter attenuating clocks and SKY62101 clock generators feature 12 outputs in a space-saving 8x8mm QFN package with wettable flanks that provide improved board level assembly reliability. These products offer the widest frequency output range from 8kHz to 3.2GHz with highly configurable clock outputs supporting multiple formats, including LVDS, HCSL, LVPECL, LVCMOS, S-LVDS, and CML. They also provide very low output-to-output skew of ±50ps with output delay adjustments as low as 50ps steps. Outputs can be independently configured for PCIe applications, with optional spread-spectrum clock generation for EMI reduction.

Combining Ethernet, PCIe, and general-purpose clock generation in a single device enables hardware designers to implement complex heterogeneous clock tree designs with the fewest number of components, simplifying design and accelerating time to market. Simplified device configuration is available using Skyworks' [ClockBuilder Pro](#) software. Additionally, these products offer excellent hitless switching with phase transients as low as 35ps while providing compliance with the ITU-T G.8262 Synchronous Ethernet (SyncE) Options 1 and 2, ITU-T G.812 Type III and IV, ITU-T G.813 Option 1, and Telcordia GR-1244 and GR-253 (Stratum-3/3E) standards.

For more information about the SKY63104/5/6 family and the SKY62101 clock generator, please visit [skyworksinc.com/Products/Timing](https://skyworksinc.com/Products/Timing).

### **About Skyworks**

Skyworks Solutions, Inc. is empowering the wireless networking revolution. We are a leading developer, manufacturer and provider of analog and mixed-signal semiconductors and solutions for numerous applications, including aerospace, automotive, broadband, cellular infrastructure, connected home, defense, entertainment and gaming, industrial, medical, smartphone, tablet and wearables.

Skyworks is a global company with engineering, marketing, operations, sales and support facilities located throughout Asia, Europe and North America and is a member of the S&P 500® market index (Nasdaq: SWKS). For more information, please visit Skyworks' website at: [www.skyworksinc.com](https://www.skyworksinc.com).

### **Safe Harbor Statement**

Any forward-looking statements contained in this media alert are intended to qualify for the safe harbor from liability established by the Private Securities Litigation Reform Act of 1995. Forward-looking statements include without limitation information relating to future events, results and expectations of Skyworks. Forward-looking statements can often be identified by words such as "anticipates," "expects," "forecasts," "intends," "believes," "plans," "may," "will" or "continue," and similar expressions and variations or negatives of these words. Actual events and/or results may

differ materially and adversely from such forward-looking statements as a result of certain risks and uncertainties including, but not limited to, our ability to timely and accurately predict market requirements and evolving industry standards and to identify opportunities in new markets; our ability to develop, manufacture, and market innovative products and avoid product obsolescence; our ability to enable solutions for the most demanding applications; our ability to compete in the marketplace and achieve market acceptance of our products; delays in the standardization or commercial deployment of 5G or AI technologies; the availability and pricing of third-party semiconductor foundry, assembly and test capacity, raw materials and supplier components; the quality of our products; our products' ability to perform under stringent operating conditions; and other risks and uncertainties identified in the "Risk Factors" section of Skyworks' most recent Annual Report on Form 10-K (and/or Quarterly Report on Form 10-Q) as filed with the Securities and Exchange Commission ("SEC"). Copies of Skyworks' SEC filings can be obtained, free of charge, on Skyworks' website ([www.skyworksinc.com](http://www.skyworksinc.com)) or at the SEC's website ([www.sec.gov](http://www.sec.gov)). Any forward-looking statements contained in this media alert are made only as of the date hereof, and we undertake no obligation to update or revise the forward-looking statements, whether as a result of new information, future events or otherwise.

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