



Skyworks Sets New Benchmark in Ultra-Low Jitter Clock Buffers for High-Speed Connectivity

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New SKY53510/80/40 Family of Clock Fanout Buffers are Purpose-Built for Data Centers, Wireless Networks, and PCIe Gen 7 Applications

IRVINE, Calif.--(BUSINESS WIRE)--Aug. 20, 2025-- Skyworks Solutions, Inc. (Nasdaq: SWKS), a global leader in high-performance analog and mixed-signal semiconductors, today unveiled the SKY53510/80/40 family of clock fanout buffers—designed to meet the stringent timing demands of next-generation high-speed infrastructure.

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solution that simplifies design and enhances signal integrity across a wide range of platforms. For system architects and hardware designers, this means faster time to market, reduced engineering overhead, and the ability to future-proof designs for emerging standards like PCIe Gen 7 and 6G wireless.

The SKY53510/80/40 family features a 3:1 input multiplexer (including crystal input), one single-ended output, and up to 10 differential outputs. Offered in compact thermally enhanced QFN packages—7x7mm (10 outputs), 6x6mm (8 outputs), and 5x5mm (4 outputs)—these devices are pin-compatible with industry-standard layouts for easy integration and multi-sourcing.

“Skyworks is pushing the boundaries of clock tree signal integrity,” said James Wilson, vice president and general manager of Mixed Signal Solutions at Skyworks. “Our new SKY53510/80/40 buffer family offers unmatched jitter performance and format flexibility—empowering designers to meet the demands of today’s most advanced communication and computing platforms.”

Designed for High-Speed Applications

SKY53510/80/40 buffers are well suited for:

- PCIe Gen 3 through Gen 7
- 56G/112G/224G SerDes
- 5G/6G mMIMO radio systems
- SyncE and broadcast video
- Medical imaging and aerospace/defense

These devices support input clock slew rates down to 0.75V/ns and output levels of 1.8V, 2.5V, and 3.3V—enabling robust PCB designs with minimal signal integrity challenges such as reflection, crosstalk, and ground bounce.

Key Features

The SKY53510/80/40 features include:

- **Ultra-low additive RMS phase jitter:** 35 fs at 156.25 MHz, 3 fs at 100 MHz (PCIe Gen 7)
- **Universal format translation:** LVPECL, LVCMOS, LVDS, HCSL, CML, SSTL, HSTL, and AC-coupled single-ended inputs; selectable LVPECL, LVDS, HCSL, or tristate outputs
- **Low power operation:** Separate core/output voltage supplies (1.8V, 2.5V, 3.3V)
- **Integrated LDOs:** >70 dBc PSRR for noisy environments
- **Wide temperature range:** -40°C to +95°C ambient (-40°C to +105°C board)
- **Low noise floor:** -166 dBc/Hz for SyncE 156.25 MHz applications

The devices are ideally suited for pairing with Skyworks’ Si551x Network Synchronizers, SKY63104/5/6 family of Jitter Attenuating Clocks and SKY62101 Ultra-low Jitter Clock Generators to deliver complete clock tree solutions that meet the demanding requirements of next-generation 6G wireless infrastructure, 800G/1600G networking infrastructure, and AI data center applications that utilize 112G/224 PAM4 SerDes technology.

Availability

Samples and production quantities of the SKY53510/80/40 clock buffers are available now. Evaluation is supported via the [SKY53510-EVB development kit](#).

For more information, please visit our [Any Format Clock Buffers page](#) or contact your Skyworks sales representative.

About Skyworks

Skyworks Solutions, Inc. is empowering the wireless networking revolution. We are a leading developer, manufacturer and provider of analog and mixed-signal semiconductors and solutions for numerous applications, including aerospace, automotive, broadband, cellular infrastructure, connected home, defense, entertainment and gaming, industrial, medical, smartphone, tablet and wearables.

Skyworks is a global company with engineering, marketing, operations, sales and support facilities located throughout Asia, Europe and North America and is a member of the S&P 500[®] market index (Nasdaq: SWKS). For more information, please visit Skyworks' website at: www.skyworksinc.com.

Safe Harbor Statement

Any forward-looking statements contained in this media alert are intended to qualify for the safe harbor from liability established by the Private Securities Litigation Reform Act of 1995. Forward-looking statements include without limitation information relating to future events, results and expectations of Skyworks. Forward-looking statements can often be identified by words such as "anticipates," "expects," "forecasts," "intends," "believes," "plans," "may," "will" or "continue," and similar expressions and variations or negatives of these words. Actual events and/or results may differ materially and adversely from such forward-looking statements as a result of certain risks and uncertainties including, but not limited to, our ability to timely and accurately predict market requirements and evolving industry standards and to identify opportunities in new markets; our ability to develop, manufacture, and market innovative products and avoid product obsolescence; our ability to compete in the marketplace and achieve market acceptance of our products; the level of widespread deployment or adoption of commercial 5G networks, AI and other new technologies; the availability and pricing of third-party semiconductor foundry, assembly and test capacity, raw materials and supplier components; the quality of our products; our products' ability to perform under stringent operating conditions; and other risks and uncertainties identified in the "Risk Factors" section of Skyworks' most recent Annual Report on Form 10-K (and/or Quarterly Report on Form 10-Q) as filed with the Securities and Exchange Commission ("SEC"). Copies of Skyworks' SEC filings can be obtained, free of charge, on Skyworks' website (www.skyworksinc.com) or at the SEC's website (www.sec.gov). Any forward-looking statements contained in this media alert are made only as of the date hereof, and we undertake no obligation to update or revise the forward-looking statements, whether as a result of new information, future events or otherwise.

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Media Relations:

Constance Griffiths
(949) 230-4867
Constance.Griffiths@skyworksinc.com

Investor Relations:

Raji Gill
(949) 508-0973
Raji.Gill@skyworksinc.com

Source: Skyworks Solutions, Inc.